



S. J. R.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Daniel Yellin et al. § Art Unit: 2611
Serial No.: 10/039,072 § Examiner: Cicely Q. Ware
Filed: January 2, 2002 § Docket: ITL.0613US
For: Robust Low Complexity Multi- § § Assignee: Intel Corporation
Antenna Adaptive Minimum
Mean Square Error Equalizer §

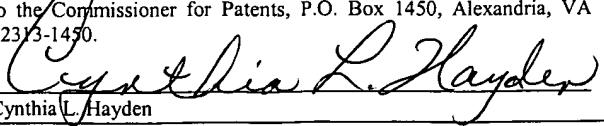
Mail Stop Issue Fee

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. § 1.312

Dear Sir:

In response to the Notice of Allowance mailed on July 31, 2006, please amend the above-referenced patent application as follows:

Date of Deposit: August 14, 2006
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Cynthia L. Hayden